

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	13	"fuel cell" and "self inspection"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:13
S2	13	"fuel cell" and self near inspect\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 11:21
S3	13	"fuel cell" and self near inspect\$4 and monitor\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 11:21
S4	1589	"fuel cell" and microprocessor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:16
S5	622	"fuel cell" and microprocessor and test\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:20
S6	34	"fuel cell" and microprocessor and self near test\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:54
S7	258	(microprocessor and self near test\$4).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 15:11
S8	220	(microprocessor and self near test\$4).ab. and @ad < "20020101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:55
S9	202	(microprocessor and self near test\$4).ab. and @ad < "20000101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:55

S10	171	(microprocessor and self near test\$4).ab. and @ad < "19980101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:55
S11	45	(microprocessor and self near test\$4).ab. and sensor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 14:58
S12	72	(microprocessor and self near test\$4) and start\$4 near state and run\$5 near state	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 15:05
S13	67	(self and (inspect\$4 test\$4 check\$4) and (fuel near cell hybrid)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 15:07
S14	10	(self and (inspect\$4 test\$4 check\$4) and (fuel near cell hybrid)).ab.	US-PGPUB; USPAT	OR	ON	2005/03/31 15:07
S15	35	(microprocessor near2 (self near test\$4)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/31 15:12
S16	0	(microprocessor near2 (self near test\$4) and register).ab.	US-PGPUB; USPAT	OR	ON	2005/03/31 15:12
S17	106	(microprocessor near2 (self near test\$4) and register)	US-PGPUB; USPAT	OR	ON	2005/03/31 15:12
S18	37	(microprocessor near2 (self near test\$4) and register and sensor)	US-PGPUB; USPAT	OR	ON	2005/03/31 15:18
S19	106	(microprocessor near2 (self near test\$4) and register)	US-PGPUB; USPAT	OR	ON	2005/03/31 15:19
S20	125	microprocessor near2 (self near (inspect\$4 detect\$4 test\$4 check\$4)) and register	US-PGPUB; USPAT	OR	ON	2005/03/31 15:21
S21	21	microprocessor same selftest	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:14
S22	9	microprocessor same selftest same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:31

S23	2	microprocessor same selftest same register same bit\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:14
S24	249	microprocessor same (selftest\$4 self near test\$4) same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:31
S25	24	microprocessor same (selftest\$4 self near test\$4) same register same pattern	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:31
S26	25	(microprocessor micro near processor) same (selftest\$4 self near test\$4) same register same pattern	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 09:47
S27	7	(microprocessor micro near processor) same (selftest\$4 self near test\$4) same register same pattern and cop\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/13 10:41